IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applicant: Art Unit: 2621

Louis Lippincott et al.

Examiner: Chikaodili E. Anyikire Serial No.: 10/750,075

Conf. No.: 2083 Filed: December 31, 2003

Docket: ITL.1703US

For: Motion Estimation Sum of all P17498

> Differences (SAD) Array Having Reduced Semiconductor Die Assignee: Intel Corporation

Area Consumption

Mail Stop RCE Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT

Sir:

Prior to the receipt of an office action, please amend the above-referenced patent application as follows:

> Date of Deposit: May 19, 2010 I hereby certify that this document is being electronically transmitted on the daté indicated above. Cynth(a.L. Hayden